

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Yoshitaka UEDA, et al.**

Confirmation No: **1390 (in parent)**

Serial No.: **09/745,990 (in parent)**

Group Art Unit: **2816 (in parent)**

Filed: **January 21, 2004**

Examiner: **An T. Luu (in parent)**

Customer No.: **38834**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 21, 2004

Sir:

Please amend the above-identified application as follows:

Attorney Docket No. 001681A
U.S. Patent Appl. No. 09/745,990 (parent)
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IN THE TITLE:

Please replace the title with the following: Semiconductor Integrated Circuit – This application is
a continuation of prior application Serial No. 09/745,990, filed on December 26, 2000.--